

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/784,219	UNO, TOSHIHIKO	
Examiner George C. Eckert II		Art Unit 2815	Page 1 of 1	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,072,216	06-2000	Williams et al.	257/339
	B	US-6,492,679 B1	12-2002	Imam et al.	257/342
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)		
	U	S. Wolf et al., Silicon Processing for the VLSI Era, Volume 1 - Process Technology, 1986, Lattice Press, pp. 26-28, 283-92.		
	V			
	W			
	X			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.